

ECSEL Pilot Line Projects

Event: ECSEL Projects Dedicated full day - Results and Benefits for European Microelectronics Industry - Pilot Lines Presentations and demonstrators (see Agenda on next slide)

Date: June 13 rd, 2018 - **Location:** Minatec, Grenoble, France

Context: Through the ECSEL JU, the European Micro- and Nano- Electronics Industry, SMEs and Research and Technology Organisations are supported and co-financed by participating States and the European Union. Goal is to contribute to the development of a strong and globally competitive electronics components and systems industry in the European Union essential for Europe's competitive leadership in the era of the digital economy.

Through this event, dedicated focus will be made on recent supported projects to highlight the positive impact they bring in terms of technical results and strategical value on the European and local Eco-system.

Audience: This dedicated ECSEL Pilot Lines full day event aims to gather key-representatives from the European Commission, French State, Regions, Collectivities, Regional Clusters, and key-partners of the Microelectronics Industry and Research and Technology Organisations involved in the European research and innovation programs

Main Objective: to share technical results and impact from these programs through illustrative successful examples of cooperation

Sponsors and Organizers:



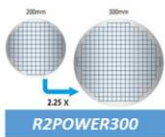
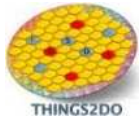
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Registration: <http://adtc2018.insight-outside.fr/>

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Agenda



Start			Topic	Speaker	
9:00	0:20	9:20	Welcome		
9:20	0:20	9:40	Opening keynote	ECSEL	Y Gigase
9:40	0:20	10:00	Digital innovation hub by Minalogic a stepping stone for microelectronic	MINALOGIC	E Mottin
10:00	1:00	11:00	FDSOI pilot lines	Chairman	ST
10:00	0:20	10:20	FDSOI eco-system build-up and benefits	ST	D Thomas
10:20	0:20	10:40	Expansion of the FDSOI-ecosystem with 22FDX from GLOBALFOUNDRIES	GF	G Teepe
10:40	0:20	11:00	FDSOI speech 3	Soitec	P Flatresse
11:00	0:30	11:30	break		
11:30	0:30	12:00	Exhibition tour (demonstrators)		
12:00	1:00	13:00	Image sensors pilot line	Chairman	TBC
12:00	0:20	12:20	POLIS Project Overview and focus on time of flight projects	ST	C Politano
12:20	0:20	12:40	Thermal Infrared sensors	ULIS	S Bethoux
12:40	0:20	13:00	3D stacked technology for Smart Image Sensors	CEA- Leti	L Arnaud
13:00	1:00	14:00	Lunch		
14:00	0:30	14:30	Exhibition tour (demonstrators)		
14:30	1:00	15:30	Power technologies	Chairman	CEA- Leti
14:00	0:30	14:30	R2Power300:		
			Project scope/goal and mission: Mario Scurati, project manager (ST, Castelletto)	ST	M Scurati
			Project exploitation and demonstration: Danilo Crippa, exploitation leader (LPE)	LPE	D Crippa
14:30	0:30	15:00	R3-PowerUP:		
			Project scope/goal and mission: Roberto Zafalon, project coordinator (ST, Agrate)	ST	R Zafalon
			Project exploitation and demonstration: Grzegorz Janczyk, Demonstration leader (ITE)	ITE	G Janczyk
15:30	0:30	16:00	break		
16:00	1:00	17:00	Embedded non volatile memory pilot line	Chairman	CEA- Leti
16:00	00:20	16:20	MCU Impact	ST	D Goubier
16:20	00:20	16:40	eNVM overview	CEA-Leti	
16:40	00:20	17:00	Terminal with STM 32H7 40nm controller	UTIA	J Kadlec
17:00	00:05	17:05	Closing and takeaway		