Minutes of the 3rd Meeting
of IFIP WG 10.5

Chiba (Japan), 1 September 1995

Attendees

• Members present:
  Raul Camposano, Luc Claesen (chair), Carlos Delgado Kloos (secretary), Franz Rummig, Manfred Glesner,
  Ganesh Gopalakrishnan, Wolf-Dieter Tiedemann (representing Werner Grass), Reiner W. Hartenstein,
  Masaharu Imai, Ricardo Reis, Wolfgang Rosenstiel, Pasupathi A. Subrahmanyam, Flavio Rech Wagner,
  Akihiko Yamada, Takayuki Yanagawa (vice-chair).

• Apologies received from:
  David Agnew, Francois Anceau, Kurt Antreich, Dominique Borzione, Franc Brgelez, Eduard Cerny, Ed-
mund M. Clarke, Francisco Corella, Werner Damm, John Darringer, Hans Eveking, Masahiro Fujita,
  Steve Furber, Dan Gale, Costas E. Goutis, Peter A. Ivey, Steven D. Johnson, Nobuaki Kawato, David C.
  Luckham, Sabine Mürz-Rössel, Paul Menchini, Jean Mermet, Gerry Musgrave, Wolfgang Nebel, Tatsuo
  Ohtsuki, Adam Pawlak, Paolo Prinetto, David J. Rees, Maringiovanna Sami, Gabrielle Saucier, Peter
  Schwarz, Jørgen Staunstrup, Tsuneta Sudo.

1. Approval of previous minutes

The minutes were approved with some changes. L. Claesen thanked T. Yanagawa for providing the room and
the working dinner. Best regards were sent to W. Grass, wishing him a good recovery from his illness.

2. Membership

The following new members were approved:

• Norbert Fristacki
• Alain Greiner
• Carlos López Barrio

Prof. Tatsuo Ohtsuki resignes and proposes Mr. Karatsu as his successor. The decision will be taken at the
next meeting. His C.V. is attached.

D. Bouldin and K. Keutzer also resign.

L. Claesen will send letters to P. Capocelli, D. Dill, F. Klaschka, C.J. Koomen, C. Mudge, R. Newton, F.
Pichler, A. Postula, M. Sami, E. Schutz, T. Sudo, Z.H. Wang, K. Wöllken asking them about their interest in
continuing as members of the WG.

3. Next meetings

The next meetings were planned as follows:

• An informal meeting will be held after that in Brighton, UK on the occasion of EuroDAC with EuroVHDL
  '95. It will be held at 18:00h on 20 September 1995 at the Metropole Hotel in Brighton.
The following meeting will be held in Paris, France on the occasion of ED&TC’96, on Tuesday, 12 March 1996, 18:30h.

It was said, that meetings should circulate around the world. After some discussion, it was decided that after the ED&TC’96 meeting, two more meetings were going to be held in 1996, one coinciding with ICCAD’96 in Santa Clara (California, USA) and one with EuroDAC’96 in Geneva (Switzerland). It was also decided that the ICCAD’96 one would be a formal one (where formal decisions were going to be taken) and the one at EuroDAC’96 on informal one (preparatory, only for information of members, with no formal votings).

4. Aims and scope

The aims and scope are to be sent by email by L. Claesen.

5. Special Interest Groups

The SIG chairs were not present.

6. Budget

The treasurer G. Musgrave was not present.

7. Past events

1. VHDL-Forum Spring 95 [Prof. P. Bakowski]

2. Workshop on Libraries, Component Modeling and Quality Assurance [Prof. D. Borrione, Dr. A. Pawlak]
   Held in conjunction with the VHDL-Forum Spring’95. Report attached.

3. ASP-DAC’95 (Asian Pacific Design Automation Conference) [Prof. T. Ohtsuki, Dr. T. Yanagawa]
   Altogether at ACV’95 (ASP-DAC’95 + CHDL’95 + VLSI’95) there were 510 attendees registered (including some non-paying). Since the break-even was at 400 attendees, some profit has been made. The loan will be returned to IFIP. Also, the IFIP tax will be paid. The Working Group gave congratulations to the organizers. Yanagawa, Rosenstiel and Johnson were thanked for their efforts towards making a success out of the three conferences.

4. CHDL’95 (Computer Hardware Description Languages and Their Applications) [Prof. S. Johnson, Prof. T. Ohtsuki]
   G. Gopalakrishnan gave a long report prepared by S. Johnson, who could not attend the meeting. Several of the discussions referred to future editions of CHDL and VLSI; they are included in the section below on “Future 10.5 conferences”. There were 9 sessions with 65, 34, 45, 28, 40, 22, 55, 25 and 23 attendees. The sessions were reasonably live, but below the average of other CHDL conferences. There will be no post-conference proceedings (the same for VLSI’95), since no other publisher could be found. The conference proceedings were published by IEEE. A special issue of Formal Methods in System Design is intended to be organized. The CHDL and VLSI papers will not appear in the CD-ROM produced by ACM. Both PC chairs (Johnson and Rosenstiel) are asked to pursue the SIGDA CD-ROM issue and clear the relevant copyright questions. If possible extended versions should be included in the CD-ROM version. It was also mentioned that long versions could be put on WWW, but these could then not be cited properly.

5. VLSI’95 [Prof. W. Rosenstiel, Prof. T. Ohtsuki]
   There were 3 no-shows. Per session there were between 20 and 200 attendees. The best paper award was given to Akihiro Tsutsui. It was pointed out that there was a decrease in the number of submissions in successive editions of VLSI (VLSI’91 around 120, VLSI’93 around 80 and VLSI’95 around 50 submissions). Nevertheless, ACV’95 should be seen as a whole, since the topics were in fact overlapping, and altogether there were around 200 papers submitted to the three conferences. M. Kishinevski replaced a speaker that could not come and did an excellent job when giving the paper. This deserves a special mention.
8. Planned events

The previous working groups WG 10.2 and (old) WG 10.5 used different principles for running their conferences, eg. all WG 10.2 members were in the PC of the CHDLs conferences, whereas this was not the case for old WG 10.5. Since the WGs have joined, unified principles have to be approved for the future. The Working Group approved the following rules: The Programme Chair and the General Chair are elected by the WG. The Programme Chair then elects the PC members, with the condition that a substantial majority of the people must be from within the WG. The PC members are expected to

- review papers
- attend the conference
- help in the distribution of conference material

1. CHDL’97
   C. Delgado Kloos had proposed Ed Cerny as PC chair. This position was confirmed and appreciated by the working group. It is planned to hold three other events in conjunction with CHDL’97: the Spring ’97 Working Conference of the VDHL Forum for CAD in Europe, the Workshop on Libraries, Component Modelling and Quality Assurance, and the Esprit NADA workshop (about New Hardware Design Methods). The final decision about the first two will be taken at the SIG-VHDL meeting on 18 September in Brighton (UK). The dates planned are 20–26 April 1997.

2. VLSI’97
   L. Claesen was approved as PC chair. R. Reis said that around 500 persons could be expected to the conference. It would be held in conjunction with the Brazilian Symposium on Microelectronic IC Design. The dates of VLSI’97 are 26–29 August 1997. R. Reis will make a concrete proposal about sponsorship of the conference. At any rate, the main (>50%) sponsor should be IFIP and the copyright should also remain with IFIP.

3. EuroDAC’95 (European Design Automation Conference)

4. EuroVHDL’95 L. Claesen read some proposals made by J. Mermet:
   - PC Chair of EuroVHDL to be appointed by SIG-VHDL. This was approved.
   - EuroVHDL PC chair shall attend EuroDAC meetings (with or without voting rights). This is a matter of EuroDAC.
   - WG 10.5 shall propose PC members for EuroDAC and EuroVHDL. The general opinion was that this was not reasonable and would only cause a deadlock.

   R. Camposano protested against the absence of J. Mermet.

5. Workshop on Logic and Architecture Synthesis
   INPG, Grenoble, 18–19 December 1995
   General chair: Gabrièle Saucier, Program Chair and Organization: Anne Mignotte
   The in cooperation status was approved.

6. 2nd International Symposium on Advanced research in Asynchronous Circuits and Systems (Async96)
   Aizu-Wakamatsu, Fukushima, Japan, 18–21 March 1996
   General Chair: Tosiyasu Kunii, Conference Co-chairs: Takashi Nanya, Alex Kondratyev
   The in cooperation status was already approved by email vote.

7. 3rd International Conference on Electronics, Circuits, and Systems (ICECS’96)
   Rodos, Greece, 13–16 October 1996
   General chair: Thanos Stouraitis, Technical Program Chair: T. Deliyannis
   The in cooperation status was not approved.
9. Future 10.5 conferences

Penino, a DAC representative who attended ACV’95, proposed that both the CHDL and VLSI conferences be held together with DAC with the same registration model as at ACV’95 (joint registration). This could happen in 1999 (DAC’99 will be held in Denver, Colorado). The general opinion was an overall agreement to this proposal, but that it should be waited for a more concrete proposal from the DAC committee. W. Rosenstiel received the mandate from the working group to act as a contact person for this issue. At any rate, a motion was approved to hold CHDL’99 and VLSI’99 together in the US. It was agreed that CHDL and VLSI could be held together every 4 or 6 years and together with some other event.