



CALL FOR PAPERS

14th IEEE International On-Line Testing Symposium

Rhodes, Greece, July 7–9, 2008



<http://tima.imag.fr/conferences/iolts>

General Chairs

M. Nicolaidis, *TIMA Laboratory*
A. Paschalis, *U. Athens*

Program Chairs

D. Gizopoulos, *U. Piraeus*
N. Seifert, *Intel*

Vice-General Chairs

R. Velazco, *TIMA Laboratory*
Y. Zorian, *Virage Logic*

Vice-Program Chairs

R. Aitken, *ARM*
C. Metra, *U. Bologna*

Local Arrangements

E. Xanthopoulos

Publications

M. Psarakis, *U. Piraeus*
N. Zergainoh, *TIMA Laboratory*

Publicity

L. Anghel, *TIMA Laboratory*
Y. Makris, *Yale U.*

Finance

N. Kranitis, *U. Athens*

Audio Visual

G. Xenoulis, *U. Piraeus*
E. Simeu, *TIMA Laboratory*

ETTTT Liaison

Z. Peng, *Linköping U.*

Program Committee

J. Abraham, *U. Texas at Austin*
D. Alexandrescu, *iRoC*
R. Baumann, *Texas Instruments*
S. Bhabhu, *Cadence*
E. Boehl, *Robert Bosch GmbH*
C. Bolchini, *Pol. di Milano*
D. Bradley, *ARM*
A. Bystrov, *U. Newcastle*
N. Buard, *EADS*
S. Chakravarty, *LSI Logic*
Y. Crouzet, *LAAS*
P. Fouillat, *IXL-ENSEIRB*
G. Georgakos, *Infineon*
P. Girard, *LIRMM*
M. Goessel, *U. Postdam*
J. Hayes, *U. Michigan*
T. Heijmen, *NXP Semiconductors*
S. Hellebrand, *U. Paderborn*
E. Ibe, *Hitachi*
A. Ivanov, *U. Brit. Columbia*
R. Iyer, *U. Illinois Urbana*
A. Krasniewski, *Warsaw U. T.*
S. Kundu, *U. Mass. Amherst*
C. Landrault, *LIRMM*
R. Leveugle, *TIMA Laboratory*
T. M. Mak, *Intel*
L. Massengill, *Vanderbilt U.*
S. Mitra, *Stanford U.*
F. Monteiro, *U. Metz*
D. Nikolos, *U. Patras*
P. Pandey, *Washington State U.*
C. Papachristou, *CWRU*
A. Papanikolaou, *IMEC*
R. Parekhji, *TI*
I. Parulkar, *Sun*
B. Paul, *Toshiba*
R. Perez, *iRoC*
M. Pflanz, *IBM Germany*
S. Piestrak, *U. Metz*
M. Pignol, *CNES*
D. Pradhan, *U. Bristol*
P. Prinetto, *Pol. di Torino*
H. Puchner, *Cypress*
D. Radaelli, *Cypress*
M. Rebaudengo, *Pol. di Torino*
K. Roy, *Purdue U.*
P. Sanda, *IBM*
J. Segura, *U. Illes Balears*
M. Sonza Reorda, *Pol. di Torino*
J. Sosnowski, *Warsaw U. T.*
L. Sourgen, *ST Microelectronics*
H. Stratigopoulos, *TIMA*
B. Straube, *Fraunhofer IIS/EAS*
J. P. Teixeira, *IST/INESC-ID*
Y. Tosaka, *Fujitsu Labs*
N. Touba, *U. Texas*
S. Tragoudas, *U. Southern Illinois*
T. Uemura, *Fujitsu Labs*
F. Vargas, *PUCRS*
X. Vera, *Intel Labs*
I. Verbauwhede, *K. U. Leuven*
M. Violante, *Pol. di Torino*
S.-J. Wen, *Cisco*
A. Wood, *Sun*
H. J. Wunderlich, *U. Stuttgart*
M. Zhang, *Intel*

Issues related to on-line testing are increasingly important in modern electronic systems. In particular, the huge complexity of electronic systems has led to growth in reliability needs in several application domains as well as pressure for low cost products. There is a corresponding increasing demand for cost-effective on-line testing techniques. These needs have increased dramatically with the introduction of very deep submicron and nanometer technologies which adversely impact noise margins and process parameters variations and make integrating on-line testing and fault tolerance mandatory in many modern ICs. The International On-Line Testing Symposium (IOLTS) is an established forum for presenting novel ideas and experimental data on these areas. The symposium also emphasizes on-line testing in the continuous operation of large applications such as wired, cellular and satellite telecommunication, as well as in secure chips. The Symposium is sponsored by the IEEE Computer Society Test Technology Technical Council and organized by TIMA Laboratory, University of Athens and University of Piraeus.

The topics of interest include (but are not limited to) the following ones:

- ❖ Reliability issues in nanometer technologies
- ❖ On-line testing of analog and mixed signal circuits
- ❖ On-line testing in the continuous operation of large systems
- ❖ On-line testing in automotive, railway, avionics, industry
- ❖ On-line current, temperature, etc, monitoring
- ❖ On-line and off-line built-in self-test
- ❖ Self-checking circuits and coding theory
- ❖ Field diagnosis, maintainability and reconfiguration
- ❖ Dependable systems design
- ❖ Dependability evaluation
- ❖ Synthesis of on-line testable circuits
- ❖ Fault-tolerant and fail-safe systems
- ❖ On-line power monitoring and control
- ❖ Radiation effects
- ❖ Secure circuit design
- ❖ Fault-based attacks and counter measures

Submissions: The IOLTS Program Committee invites authors to submit papers in the above or related technical areas. Accepted papers will be included in formal Proceedings to be published by the IEEE. Papers must be submitted electronically via the Symposium web site.

The *IEEE Transactions on Dependable and Secure Computing (TDSC)* is actively soliciting high quality papers in the areas of hardware dependability and security. IOLTS 2008 authors will be invited to submit archival versions of their papers for consideration for publication in IEEE TDSC.

Please observe the following key dates:

Submission deadline: **February 20 2008**

Notification of acceptance: **April 8 2008**

Camera-ready papers due: **April 25 2008**

Submission Information

Dimitris Gizopoulos

University of Piraeus
Department of Informatics
80 Karaoli & Dimitriou Str.
18534, Piraeus, Greece
Tel: +30 210 414 2372
dgizop@unipi.gr

Norbert Seifert

Intel Corporation
Logic Techn.Development Q&R
5200 N. E. Elam Young Pk
Hillsboro, OR 97124-5503, USA
Tel: +1 971 214 1700
norbert.seifert@intel.com

General Information

Antonios Paschalis

University of Athens
Department of Informatics
Panepistimiopolis
15784 Athens, Greece
Tel: +30 210 727 5231
paschali@di.uoa.gr

Michael Nicolaidis

TIMA Laboratory
46, av. Felix Viallet
38031 Grenoble
France
Tel: +33 (0) 4 76 57 46 96
Michael.Nicolaidis@imag.fr

About Rhodes: Rhodes called from its local people the Rose of the Aegean and deserves its name because is one of the most beautiful Greek islands and one of the most popular holiday destinations in the Mediterranean. Rhodes has been famous since antiquity as the site of Colossus of Rhodes, one of the Seven Wonders of the World. The citadel of Rhodes, built by the Knights Hospitalliers, is one of the best preserved medieval towns in Europe. The ancient city of Lindos and the Valley of the Butterflies are other of the main attractions of the island.

About the Venue: IOLTS 2008 will be held in Rodos Palace, the Finest Deluxe Resort Complex on the island (<http://www.rodos-palace.gr/>).

For all updated information concerning IOLTS 2008, please visit the IOLTS web site:
<http://tima.imag.fr/conferences/iolts>



IEEE Computer Society



Test Technology Technical Council