

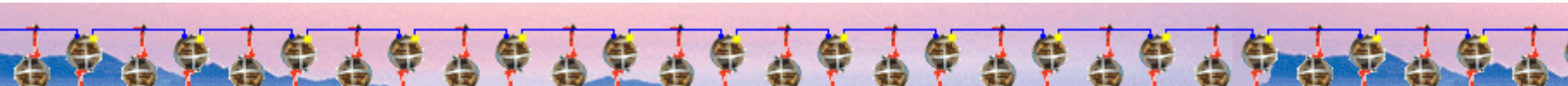


Program Chairs

- Alex Yakolev, Univ. of Newcastle
- Jens Sparsø, TU Denmark (DTU)

Invited Speakeres

- Steve Furber, Univ. of Manchester



51 Papers Submitted

Canada	3	Poland	1
China	3	Spain	1
France	6	Sweden	1
India	3	Switzerland	1
Israel	2	Taiwan	1
Italy	1	UK	8
Japan	3	USA	17

+ Singapore, The Netherlands

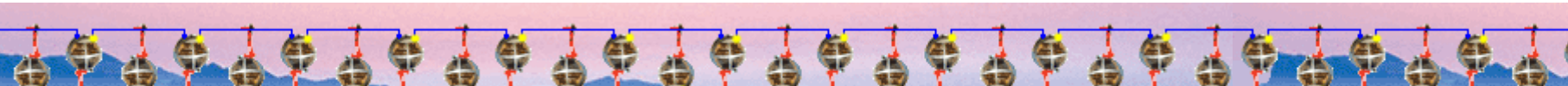


The review process

- 5 reviews per paper
- 41 reviewers
- 6-7 papers per reviewer

- 19 of the 51 papers accepted (37 %)

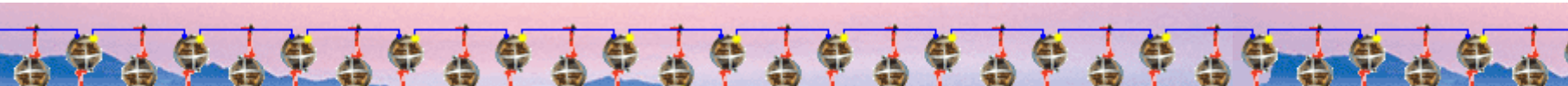
- The 32 rejected papers: comments and suggestions for improvements





The program

- 3 invited speakers
- 19 papers in 7 sessions
- 1 industrial session



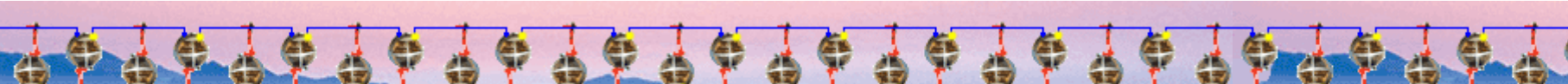


Monday

Invited talk

- **Asynchronous Design: An Enabler for Flexible Microelectronics**
Nobuo Karaki, *Seiko Epson Corp.*

Paper sessions

- Session 1: Interfacing and Synchronization
 - Session 2: Fault-tolerance and testing
 - Session 3: Novel architectures and Design practices
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Tuesday

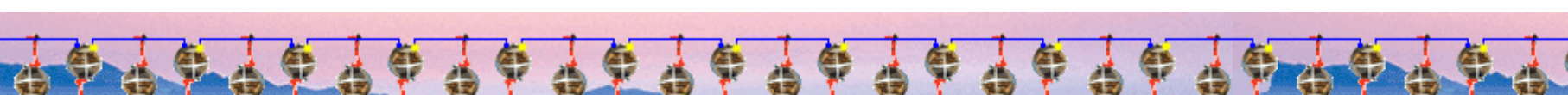
Invited talk

- ATRS: an alternative roadmap for semiconductors, technology evolution and impacts on system architecture
Jean-Pierre Schoellkopf, *STMicroelectronics*

Paper sessions:

- Session 4: Interconnect and Communications
- Session 5: Synthesis
- Industrial session

Social event





Wednesday

Invited talk

- **Asynchronous Architectures for Nanometer Scales.** Ferdinand Peper. *National Institute of Information and Communications Technology, Japan*

Paper sessions

- Session 6: Design and Architectures for GALS
- Session 7: Slack Matching
- Best Paper Award & Closing Session

